

In re Patent Application of:  
**LEAMING**  
Serial No. 10/829,007  
Filing Date: April 21, 2004

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In the Claims:

This listing of claims replaces all prior versions and listing of claims in the application.

1. (Currently amended) An integrated circuit for a smart card comprising:  
a transceiver; and  
a processor for communicating with a host device over a system bus via said transceiver, said processor for  
providing at least one default descriptor to the host device,  
cooperating with the host device to perform an enumeration based upon the at least one default descriptor, and  
~~detecting a system utilization metric exceeding a threshold and, responsive thereto,~~ providing at least one alternate descriptor to the host device and cooperating with the host device to perform a new enumeration based thereon, ~~the system utilization metric based upon a number of other devices communicating with the host device~~ based upon allocations of system bus bandwidth to other devices communicating with the host device over the system bus.

2. (Cancelled).

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3. (Cancelled).

4. (Original) The integrated circuit of Claim 1 wherein the at least one alternate descriptor comprises at least one device descriptor.

5. (Original) The integrated circuit of Claim 1 wherein the at least one alternate descriptor comprises at least one configuration descriptor.

6. (Original) The integrated circuit of Claim 1 wherein the at least one alternate descriptor comprises at least one interface descriptor.

7. (Original) The integrated circuit of Claim 1 wherein the at least one alternate descriptor comprises at least one endpoint descriptor.

8. (Original) The integrated circuit of Claim 1 further comprising at least one memory connected to said processor for storing the at least one default descriptor and the at least one alternate descriptor.

9. (Original) The integrated circuit of Claim 1 wherein said transceiver comprises a universal serial bus (USB) transceiver, and wherein said processor operates in a USB mode.

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10. (Currently amended) A smart card comprising:  
a smart card body; and  
an integrated circuit carried by said smart card body  
and comprising

a transceiver, and  
a processor for communicating with a host device  
over a system bus via said transceiver, said processor  
for

providing at least one default descriptor to  
the host device,

cooperating with the host device to perform  
an enumeration based upon the at least one default  
descriptor, and

~~detecting a system utilization metric~~  
~~exceeding a threshold and, responsive thereto,~~  
providing at least one alternate descriptor to the  
host device and cooperating with the host device  
to perform a new enumeration based thereon, ~~the~~  
~~system utilization metric based upon a number of~~  
~~other devices communicating with the host device~~  
based upon allocations of system bus bandwidth to  
other devices communicating with the host device  
over the system bus.

11. (Cancelled).

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12. (Cancelled).

13. (Original) The smart card of Claim 10 wherein the at least one alternate descriptor comprises at least one device descriptor.

14. (Original) The smart card of Claim 10 wherein the at least one alternate descriptor comprises at least one configuration descriptor.

15. (Original) The smart card of Claim 10 wherein the at least one alternate descriptor comprises at least one interface descriptor.

16. (Original) The smart card of Claim 10 wherein the at least one alternate descriptor comprises at least one endpoint descriptor.

17. (Original) The smart card of Claim 10 wherein said integrated circuit further comprises at least one memory connected to said processor for storing the at least one default descriptor and the at least one alternate descriptor.

18. (Original) The smart card of Claim 10 wherein said transceiver comprises a universal serial bus (USB) transceiver, and wherein said processor operates in a USB mode.

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19. (Currently amended) A smart card system comprising:

a host device and associated system bus;

a smart card adapter connected to said host device via said system bus; and

a smart card to be read by said smart card adapter and comprising a smart card body and an integrated circuit carried by said smart card body, said integrated circuit comprising

a transceiver, and

a processor for communicating with said host device over said system bus via said transceiver, said processor for

providing at least one default descriptor to said host device,

cooperating with said host device to perform an enumeration based upon the at least one default descriptor, and

~~detecting a system utilization metric exceeding a threshold and, responsive thereto,~~  
providing at least one alternate descriptor to said host device and cooperating with said host device to perform a new enumeration based thereon,  
~~the system utilization metric based upon a number of other devices communicating with the host device~~  
based upon allocations of system bus

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bandwidth to other devices communicating with said  
host device over said system bus.

20. (Cancelled).

21. (Cancelled).

22. (Original) The smart card system of Claim 19 wherein the at least one alternate descriptor comprises at least one device descriptor.

23. (Original) The smart card system of Claim 19 wherein the at least one alternate descriptor comprises at least one configuration descriptor.

24. (Original) The smart card system of Claim 19 wherein the at least one alternate descriptor comprises at least one interface descriptor.

25. (Original) The smart card system of Claim 19 wherein the at least one alternate descriptor comprises at least one endpoint descriptor.

26. (Original) The smart card system of Claim 19 wherein said integrated circuit further comprises at least one

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memory connected to said processor for storing the at least one default descriptor and the at least one alternate descriptor.

27. (Original) The smart card system of Claim 19 wherein said transceiver comprises a universal serial bus (USB) transceiver, and wherein said host device and said processor operate in a USB mode.

28. (Currently amended) A method for operating a smart card comprising:

providing at least one default descriptor from the smart card to a host device over a system bus;

cooperating with the host device over the system bus to perform an enumeration based upon the at least one default descriptor; and

~~detecting a system utilization metric exceeding a threshold and, responsive thereto,~~ providing at least one alternate descriptor to the host device and cooperating with the host device over the system bus to perform a new enumeration based thereon, ~~the system utilization metric based upon a number of other devices communicating with the host device~~ based upon allocations of system bus bandwidth to other devices communicating with the host device over the system bus.

29. (Cancelled).

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30. (Cancelled).

31. (Original) The method of Claim 28 wherein the at least one alternate descriptor comprises at least one device descriptor.

32. (Original) The method of Claim 28 wherein the at least one alternate descriptor comprises at least one configuration descriptor.

33. (Original) The method of Claim 28 wherein the at least one alternate descriptor comprises at least one interface descriptor.

34. (Original) The method of Claim 28 wherein the at least one alternate descriptor comprises at least one endpoint descriptor.

35. (Original) The method of Claim 28 wherein the smart card comprises a universal serial bus (USB) smart card.

Claims 36-41 (cancelled).